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CS-350

Final Project

Our architecture decisions are decisions between TI, Microchip, & Freescale. All of these architectures are different types of microcontrollers. Microchip uses 64KB of flash & 4kb of RAM. These are mainly used for things like clocks or calendars with CTMU peripherals. These lack the ability for our broadband devices which takes this system out of the running.

Next we have our Freescale architecture these devices are aimed at high performance with low power. With up to a 1.3Ghz processor this single chip has more capabilities than the TI. This architecture also has built in broadband access equipment which is used for radio signals or WiFi/LTE towers.

Finally we have the TI architecture which is the best bang for your buck. Costing only around $2 dollars a piece this device is the microcontroller for it’s 38 pins as well as it’s advanced low power usage and modern LED’s with it’s battery powered options as well. These TI devices also support a broad range of broadband equipment.

In conclusion our options fall down to the Freescale & TI architecture since they are the only ones that support WIFI. This in mind both options give us the RAM and power we need marking our second check box. All things considered we no real downfalls by using the TI it seems to be the better of both options due to it’s cheaper price.